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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10044344	01/10/2002	257	750	2844	William S

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**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	1016-012
Verified and Acknowledged Examiners's initials		
TITLE : Via-sea layout integrated circuits		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims: 10 Print Claim for O.G.	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheet 1 of 1	Print File
		Application Examiner	
<input type="checkbox"/> TERMINAL		PREPARED FOR ISSUE	
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